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THIRD SEMESTER (CBCSS—UG) DEGREE EXAMINATION NOVEMBER 2021

Electronics

ELE 3C 05—DIGITAL ELECTRONICS

(2019—2020 Admissions)

Time: Two Hours

Maximum: 60 Marks

Section A

Answer at least **eight** questions.

Each question carries 3 marks.

All questions can be attended.

Overall Ceiling 24.

- 1. What are alphanumeric codes? For what they are used?
- 2. What is 1' complement representation method?
- 3. State and prove associative law of Boolean algebra.
- 4. What is a Maxterm?
- 5. Can a multiplexer be used to realize logic functions? If yes, in what ways this realization is better than realization using logic gates?
- 6. What is an Encoder?
- 7. What do you mean by toggling?
- 8. What are the types of loading in a shift register?
- 9. What are the asynchronous inputs of a flip-flop?
- List any four applications of counters.
- 11. What are RAMs? How they differ from ROMs?
- 12. Is ROM a volatile memory? Explain.

 $(8 \times 3 = 24 \text{ marks})$

Turn over

Section B

Answer at least **five** questions. Each question carries 5 marks. All questions can be attended. Overall Ceiling 25.

- 13. Given $A\overline{B} + \overline{AB} = C$, show that $A\overline{C} + \overline{AC} = B$.
- 14. Discuss hexadecimal number system.
- 15. Realize the logic expression using MUX. $F1 = \sum m(1, 3, 4, 7, 12, 14, 15)$.
- 16. Realize a full adder using NAND gates only.
- 17. With neat diagrams, explain the working of 4-bit ring counter.
- 18. With neat diagrams, explain the operation of T and D flip-flops.
- 19. What are the different types of ROMs? Explain.

 $(5 \times 5 = 25 \text{ marks})$

Section C

Answer any **one** question.

The question carries 11 marks.

- 20. What are the different types of gates? Explain in detail each of them.
- 21. Design a 3 bit synchronous up counter.

 $(1 \times 11 = 11 \text{ marks})$